

IN THE CLAIMS

This listing of the claim will replace all prior versions and listings of claim in the present application.

Listing of Claims

1. (currently amended) A circuit simulation method for carrying out operational simulation of an electronic circuit through a network, said circuit simulation method comprising the steps of:

sending, from a first information processing apparatus connected with said network, circuit data indicating characteristics of said electronic circuit through said network;

receiving said circuit data at a second information processing apparatus connected with said network, said second information processing apparatus being so arranged that circuit models describing electronic circuit operations are stored therein to enable operational simulation ~~thereon~~, said circuit models being stored in a storage accessible based on predefined authorization;

carrying out operational simulation of said electronic circuit at said second information processing apparatus using said circuit models and said circuit data; and

sending results of said operational simulation from said second information processing apparatus through said network.

2. (original) A circuit simulation method as claimed in claim 1,

wherein, at said step of sending results of said operational simulation, said results of said operational simulation are sent to said first information processing apparatus from said second information processing apparatus through said network.

3. (currently amended) A circuit simulation method as claimed in claim 1, wherein each of said circuit models includes device models, each device model indicating operations of a device constituting a circuit.

4. (currently amended) A circuit simulation method as claimed in claim 3, wherein each of said device models includes transistor models, each transistor model indicating operations of a transistor constituting said circuit.

5. (currently amended) A circuit simulation apparatus for carrying out operational simulation of an electronic circuit through a network, said circuit simulation apparatus comprising:

means for receiving, from a first information processing apparatus connected with said network, circuit data indicating characteristics of said electronic circuit through said network;

means for storing circuit models describing electronic circuit operations, the stored circuit models being accessible based on predefined authorization;

means for carrying out operational simulation of said electronic circuit using said circuit models and said circuit data; and

means for sending results of said operational simulation through said network.

6. (original) A circuit simulation apparatus as claimed in claim 5, wherein said means for sending results of said operational simulation sends said results of said operational simulation to said first information processing apparatus through said network.

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7. (currently amended) A circuit simulation apparatus as claimed in claim 5, wherein each of said circuit models includes device models, each device model indicating operations of a device constituting a circuit.

8. (currently amended) A circuit simulation apparatus as claimed in claim 7, wherein each of said device models includes transistor models, each transistor model indicating operations of a transistor constituting said circuit.

9. (currently amended) A computer program product storable on a storage medium for carrying out operational simulation of an electronic circuit through a network in a circuit simulation system where said computer program product is run on a computer in which circuit models describing electronic circuit operations are stored, said computer program product comprising the steps of:

receiving, from a first information processing apparatus connected with said network, circuit data indicating characteristics of an electronic circuit through said network,

wherein said circuit models are stored in a storage and are accessible based on predefined authorization;

carrying out operational simulation of said electronic circuit using said circuit models and said circuit data; and

sending results of said operational simulation through said network.

10. (original) A computer program product as claimed in claim 9, wherein, at said step of sending results of said operational simulation, said results of said operational simulation are sent to said first information processing apparatus from said second information processing apparatus through said network.

11. (currently amended) A computer program product as claimed in claim 9, wherein each of said circuit models includes device models, each device model indicating operations of a device constituting a circuit.

12. (currently amended) A computer program product as claimed in claim 11,

wherein each of said device models includes transistor models, each transistor model indicating operations of a transistor constituting said circuit.

13. (new): A circuit simulation method as claimed in claim 1, said storage means is divided into a plurality of regions.

14. (new): A circuit simulation method as claimed in claim 1, said storage means is divided into a plurality of regions and the person who has authority of each region can access the corresponding region.

15. (new): A circuit simulation method as claimed in claim 13, said storage means is divided into three regions, a first and second region is capable of being accessed by a server manager, and a third region is capable of being accessed by a device supplier.


16. (new): A circuit simulation method as claimed in claim 15, the first region is the region for holding mounting rules, the second region is the region for holding simulation circuit connection models, and the third region is the region for holding device transistor models and/or device models.

17. (new): A circuit simulation method as claimed in claim 1, a user-specific simulation model data is stored in said storage means.

18. (new): A circuit simulation method as claimed in claim 15, a user-specific simulation model data is stored in said third region in said storage means.

19. (new): A circuit simulation method as claimed in claim 17, a user-specific simulation model data is stored in said third region in said storage means.

20. (new): A circuit simulation method as claimed in claim 18, said third region is capable of being accessed by a user who supply the user-specific simulation model data.

 21. (new): A circuit simulation apparatus as claimed in claim 5, said means for storing is divided into a plurality of regions.

22. (new): A circuit simulation apparatus as claimed in claim 5, said means for storing is divided into a plurality of region and the person who has authority of each region can access the corresponding region.

23. (new): A circuit simulation apparatus as claimed in claim 21, said means for storing is divided into three regions, a first and second region is capable of being accessed by a server manager, and a third region is capable of being accessed by a device supplier.

24. (new): A circuit simulation apparatus as claimed in claim 23, the first region is the region for holding mounting rules, the second region is the region for holding simulation circuit connection models, and the third region is the region for holding device transistor models and/or device models.

25. (new): A circuit simulation apparatus as claimed in claim 5, wherein a user-specific simulation model data is stored in said means for storing.

26. (new): A circuit simulation apparatus as claimed in claim 22, wherein a user-specific simulation model data is stored in said third region in said means for storing.

27. (new): A circuit simulation apparatus as claimed in claim 24, wherein a user-specific simulation model data is stored in said third region in said means for storing.

28. (new): A circuit simulation apparatus as claimed in claim 26, said third region is capable of being accessed by a user who supply the user-specific simulation model data.

29. (new): A computer program product as claimed in claim 9, said storage means is divided into a plurality of regions.

30. (new): A computer program product as claimed in claim 9, said storage means is divided into a plurality of regions and the person who has authority of each region can access the corresponding region.

31. (new): A computer program product as claimed in claim 29, said storage means is divided into three regions, a first and second region is capable of being accessed by a server manager, and a third region is capable of being accessed by a device supplier.

32. (new): A computer program product as claimed in claim 31, the first region is the region for holding mounting rules, the second region is the region for holding simulation circuit connection models, and the third region is the region for holding device transistor models and/or device models.

33. (new): A computer program product as claimed in claim 9, a user-specific simulation model data is stored in said storage means.

34. (new): A computer program product as claimed in claim 29, a user-specific simulation model data is stored in said third region in said storage means.

35. (new): A computer program product as claimed in claim 31, a user-specific simulation model data is stored in said third region in said storage means.

36. (new): A computer program product as claimed in claim 34, said third region is capable of being accessed by a user who supply the user-specific simulation model data.
